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**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2816**

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Response  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TANIGUCHI

Art Unit: 2816

Application No. 09/777,897

Examiner: L. Nguyen

Filed: February 7, 2001

Atty. Docket No. 100353-00039

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TECHNOLOGY CENTER 28000

For: DELAY TIME ADJUSTING METHOD OF DELAYING A PHASE OF AN OUTPUT SIGNAL UNTIL A PHASE DIFFERENCE BETWEEN AN INPUT SIGNAL AND THE OUTPUT SIGNAL BECOMES AN INTEGRAL NUMBER OF PERIODS OTHER THAN ZERO

**REQUEST FOR RECONSIDERATION UNDER 35 U.S.C. § 1.116**

Commissioner for Patents  
Washington, D.C. 20231

March 24, 2003

Sir:

In reply to the Office Action mailed December 24, 2002, reconsideration of the above-identified application is respectfully requested in view of the following remarks.

**REMARKS**

The Office Action dated December 24, 2002 has been received and carefully noted. The following remarks, are submitted as a full and complete response thereto. Accordingly, favorable consideration of claims 1-8 is respectfully requested.

Applicant gratefully appreciates the courtesies extended by the Examiner to the Applicant's representative, Jonathan Kidney, during the Personal Interview conducted on